



PATENT  
81788.0025

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re U.S. Patent 6,963,102

Inventors: Seiichi MORI

Issued: November 8, 2005

Appln. No.: 09/451,619

Filed: November 30, 1999

For: Non-Volatile Semiconductor Memory  
Having A Decreased Gate Length

Art Unit: 2814

Examiner: Weiss, Howard

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to:

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**REQUEST FOR CERTIFICATE OF CORRECTION**

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Dear Sir:

Request is hereby made for correction of the subject Letters Patent.

- ☒ It is believed this is applicant's error, and a fee of \$100 is enclosed. However, if any adjustment in the fee is necessary please use Deposit Account No. 50-1314 of the undersigned attorney's firm.
- ☐ It is believed this is a Patent Office error, and accordingly, no fee is enclosed. However if a fee should be required, please charge it to Deposit Account No. 50-1314 of the undersigned attorney's firm.

A copy of this Request is enclosed.

01/24/2006 FFANAI2 00000002 501314 6963102

01 FC:1811 100.00 DA

Date: January 12, 2006

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Respectfully submitted,

HOGAN & HARTSON L.L.P.

By: \_\_\_\_\_

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**Certificate**

JAN 25 2006

**of Correction**

JAN 26 2006

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(Also Form PTO-1050)

## UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : 6,963,102

DATED : November 8, 2005

INVENTOR(S): Seiichi MORI

It is certified that error appears in the above-identified patent and that said Letters Patent are hereby corrected as shown below:

Front Page, Col. 1, line 73, change "Kabushiki Karsha Toshiba" to -KABUSHIKI KAISHA TOSHIBA--.

MAILING ADDRESS OF SENDER: Troy M. Schmelzer, Reg. No. 36,667  
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PATENT NO. 6,963,102

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